

Notic of References Cited	Application/Control No. 10/065,750	Applicant(s)/Patent Under Reexamination LIU ET AL.	
	Examiner Chuong A Luu	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,538,270 B1	03-2003	Randolph et al.	257/208
	B	US-6,376,341 B1	04-2002	Kluth et al.	438/510
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wofl et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Volume 1, Pages 532-534.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.